

(75) precharg\$4 with ("same" adj2 time) with address
(110842) dram or (dynamic adj3 memory)
(42) (precharg\$4 with ("same" adj2 time) with address) and (dram or (dynamic adj3 memory))
(33) (precharg\$4 with ("same" adj2 time) with address) not ((precharg\$4 with ("same" adj2 time)
(63727) address near5 decod\$4
(1135) precharg\$4 with (address near5 decod\$4)
(3669) second adj5 memory adj5 access
(39) precharg\$4 with (second adj5 memory adj5 access)
(4938) 365/203
(5217) 365/233
(2206) 365/189.04
(4940) 365/189.05
(1071) 365/191
(2411) 365/194
(950) precharg\$4 with (memory adj5 access)
(252) (precharg\$4 with (memory adj5 access)) and 365/203
(161) (precharg\$4 with (memory adj5 access)) and 365/233
(84) (precharg\$4 with (memory adj5 access)) and 365/189.05
(8) (precharg\$4 with (memory adj5 access)) and 365/191
(45) (precharg\$4 with (memory adj5 access)) and 365/194
(66) (precharg\$4 with (memory adj5 access)) and 365/189.04
(6108) 365/230.03
(732) 365/230.04
(214) (precharg\$4 with (memory adj5 access)) and 365/230.03
(23) (precharg\$4 with (memory adj5 access)) and 365/230.04
(15) (DEMON near2 PAUL) in.

Q5e	USPAT-US-PGPUB E P Durs
Default operator	OR <input checked="" type="checkbox"/> Highlight all hit terms only

	U	1	Document ID	Issue Date	Inventor	Current OR	Current XRs	Retrieval C	Title	Page	S	C	P	3
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5657295 A	19970812	Rao, G. R. Mohan	365/203	365/230.03; 365/230.04;		Pipelined address memories and systems and methods us	9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6067274 A	20000523	Yoshimoto, Takahiko	365/238.5	365/189.02; 365/203		Semiconductor memory device having a burst mode	19	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6091629 A	20000718	Osada, Kenichi et al.	365/156	365/154; 365/220;		High speed semiconductor memory apparatus including	30	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6650573 B2	20031118	Sunaga, Toshio et al.	365/189.05	365/233; 711/167		Data input/output method	12	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>